

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the application of:

Georg Rudolf SILLNER

Serial No.:

Group Art Unit:

Filed :

Examiner:

For : **PROCESS FOR PROCESSING ELECTRIC COMPONENTS,
ESPECIALLY SEMICONDUCTOR CHIPS, AND
APPARATUS FOR IMPLEMENTING THIS PROCESS**

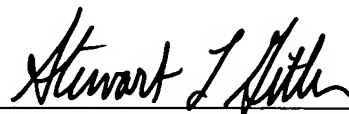
INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with applicant's duty of disclosure, applicant submits a listing of documents on PTO Form 1449 which the Examiner may find relevant to the examination of the application. These documents were listed in the International Search Report (copy enclosed). A copy of each of the foreign documents listed is enclosed.

Respectfully submitted,



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September 29, 2004

Attorney Docket No: A-9249.IDS/eb

INFORMATION DISCLOSURE CITATION
(Use several sheets if necessary)

Docket Number (Optional)

A-9249

DT04 Rec'd

Application Number

01/508948

04 OCT 2004

Applicant(s)

Georg Rudolf SILLNER

Filing Date

Group Art Unit

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

U.S. PATENT APPLICATION PUBLICATIONS

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO
		0 075 491	1983-03-30	EP				
		0 146 197	1985-06-26	EP				

OTHER DOCUMENTS

(Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.